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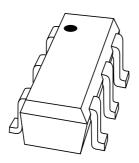
If you have any questions related to the data sheet, please contact our nearest sales office via e-mail or telephone (details via **salesaddresses@nexperia.com**). Thank you for your cooperation and understanding,

Kind regards,

Team Nexperia

# DISCRETE SEMICONDUCTORS

# DATA SHEET



# BC857BS PNP general purpose double transistor

Product data sheet Supersedes data of 1997 Jul 09 1999 Apr 26



# PNP general purpose double transistor

**BC857BS** 

### **FEATURES**

- Low collector capacitance
- Low collector-emitter saturation voltage
- · Closely matched current gain
- · Reduces number of components and boardspace
- No mutual interference between the transistors.

### **APPLICATIONS**

• General purpose switching and amplification.

### **DESCRIPTION**

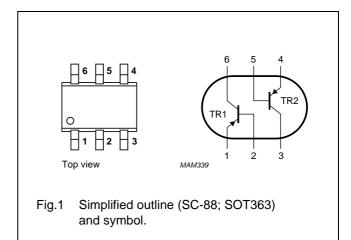
PNP double transistor in an SC-88; SOT363 plastic package. NPN complement: BC847BS.

### **MARKING**

TYPE NUMBER	MARKING CODE
BC857BS	3Ft

### **PINNING**

PIN	DESCRIPTION		
1, 4	emitter	TR1; TR2	
2, 5	base	TR1; TR2	
6, 3	collector	TR1; TR2	



### **LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT		
Per transis	Per transistor						
V <sub>CBO</sub>	collector-base voltage	open emitter	_	-50	V		
$V_{CEO}$	collector-emitter voltage	open base	-	-45	V		
$V_{EBO}$	emitter-base voltage	open collector	-	-5	V		
I <sub>C</sub>	collector current (DC)		-	-100	mA		
I <sub>CM</sub>	peak collector current		-	-200	mA		
I <sub>BM</sub>	peak base current		-	-200	mA		
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	-	200	mW		
T <sub>stg</sub>	storage temperature		-65	+150	°C		
Tj	junction temperature		_	150	°C		
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C		
Per device	Per device						
P <sub>tot</sub>	total power dissipation	Tamb ≤ 25 °C; note 1	-	300	mW		

### Note

1. Device mounted on an FR4 printed-circuit board.

# PNP general purpose double transistor

**BC857BS** 

### THERMAL CHARACTERISTICS

SYMBOL PARAMETER		CONDITIONS	VALUE	UNIT
Per device				
R <sub>th j-a</sub>	thermal resistance from junction to ambient	note 1	416	K/W

### Note

1. Device mounted on an FR4 printed-circuit board.

### **CHARACTERISTICS**

 $T_{amb}$  = 25 °C unless otherwise specified.

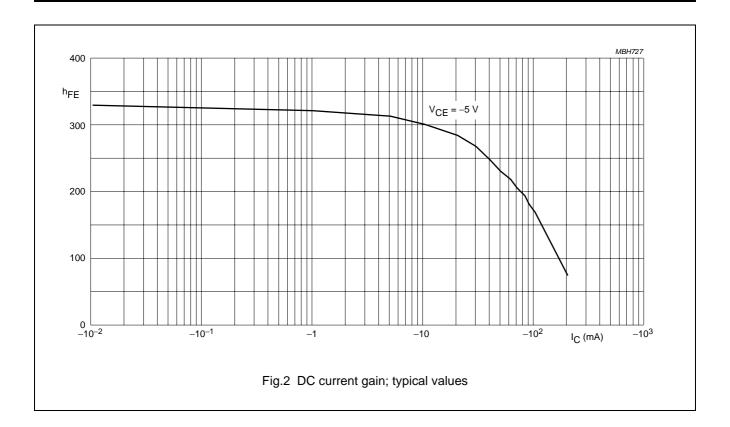
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT		
Per transist	Per transistor							
I <sub>CBO</sub>	collector cut-off current	$I_E = 0; V_{CB} = -30 \text{ V}$	_	-	-15	nA		
		I <sub>E</sub> = 0; V <sub>CB</sub> = -30 V; T <sub>j</sub> = 150 °C	_	_	-5	μΑ		
I <sub>EBO</sub>	emitter cut-off current	$I_C = 0; V_{EB} = -5 \text{ V}$	_	-	-100	nA		
h <sub>FE</sub>	DC current gain	$I_C = -2 \text{ mA}; V_{CE} = -5 \text{ V}$	200	_	450			
V <sub>CEsat</sub>	collector-emitter saturation	$I_C = -10 \text{ mA}; I_B = -0.5 \text{ mA}$	_	_	-100	mV		
	voltage	$I_C = -100 \text{ mA}; I_B = -5 \text{ mA}; \text{ note 1}$	_	-	-400	mV		
V <sub>BEsat</sub>	base-emitter saturation voltage	$I_C = -10 \text{ mA}; I_B = -0.5 \text{ mA}$	_	-755	_	mV		
$V_{BE}$	base-emitter voltage	$I_C = 2 \text{ mA}; V_{CE} = -5 \text{ V}$	-600	-655	-750	mV		
C <sub>c</sub>	collector capacitance	$I_E = i_e = 0$ ; $V_{CB} = -10 \text{ V}$ ; $f = 1 \text{ MHz}$	_	-	2.2	pF		
Ce	emitter capacitance	$I_C = i_c = 0$ ; $V_{EB} = -500 \text{ mV}$ ; $f = 1 \text{ MHz}$	_	10	_	pF		
f <sub>T</sub>	transition frequency	$I_C = -10 \text{ mA}; V_{CE} = -5 \text{ V}; f = 100 \text{ MHz}$	100	_	_	MHz		

### Note

1. Pulse test:  $t_p \le 300~\mu s;~\delta \le 0.02.$ 

# PNP general purpose double transistor

BC857BS



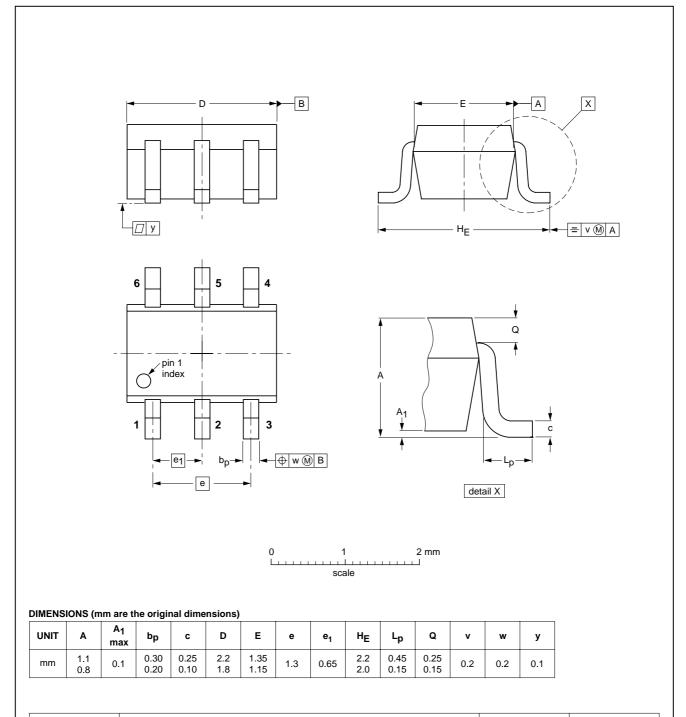
# PNP general purpose double transistor

**BC857BS** 

### **PACKAGE OUTLINE**

Plastic surface mounted package; 6 leads

**SOT363** 



OUTLINE		REFERENCES			EUROPEAN	ISSUE DATE	
	VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
	SOT363			SC-88			97-02-28

### PNP general purpose double transistor

**BC857BS** 

### **DATA SHEET STATUS**

DOCUMENT STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)</sup>	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

### **Notes**

- 1. Please consult the most recently issued document before initiating or completing a design.
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### **Customer notification**

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### **Contact information**

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