# **General Purpose Transistors**

# **NPN Silicon**

#### **Features**

- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	$V_{CEO}$	32	Vdc
Collector-Base Voltage	$V_{CBO}$	32	Vdc
Emitter-Base Voltage	V <sub>EBO</sub>	5.0	Vdc
Collector Current – Continuous	I <sub>C</sub>	100	mAdc

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

#### THERMAL CHARACTERISTICS

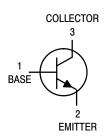
Characteristic	Symbol	Value	Unit
Total Device Dissipation FR-5 Board <sup>(1)</sup> T <sub>A</sub> = 25°C	P <sub>D</sub>	225	mW
Derate above 25°C		1.8	mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{ heta JA}$	556	°C/W
Total Device Dissipation Alumina Substrate, (2) T <sub>A</sub> = 25°C	P <sub>D</sub>	300	mW
Derate above 25°C		2.4	mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	417	°C/W
Junction and Storage Temperature	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

- 1. FR-5 =  $1.0 \times 0.75 \times 0.062$  in.
- 2. Alumina = 0.4  $\times$  0.3  $\times$  0.024 in. 99.5% alumina.



# ON Semiconductor®

#### www.onsemi.com





SOT-23 (TO-236) CASE 318 STYLE 6

# MARKING DIAGRAM



D2 = Device Code M = Date Code\* • = Pb-Free Package

(Note: Microdot may be in either location)

# **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
BCW32LT1G	SOT-23 (Pb-Free)	3000 / Tape & Reel
NSVBCW32LT1G	SOT-23 (Pb-Free)	3000 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

<sup>\*</sup>Date Code orientation and/or overbar may vary depending upon manufacturing location.

# **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS			•		
Collector – Emitter Breakdown Voltage $(I_C = 2.0 \text{ mAdc}, V_{EB} = 0)$	V <sub>(BR)CEO</sub>	32	_	-	Vdc
Collector – Base Breakdown Voltage $(I_C = 10 \mu Adc, I_E = 0)$	V <sub>(BR)CBO</sub>	32	-	_	Vdc
Emitter – Base Breakdown Voltage ( $I_E = 10 \mu Adc$ , $I_C = 0$ )	V <sub>(BR)EBO</sub>	5.0	-	_	Vdc
Collector Cutoff Current $(V_{CB} = 32 \text{ Vdc}, I_E = 0)$ $(V_{CB} = 32 \text{ Vdc}, I_E = 0, T_A = 100^{\circ}\text{C})$	I <sub>CBO</sub>	_ _	- -	100 10	nAdc μAdc
ON CHARACTERISTICS					
DC Current Gain (I <sub>C</sub> = 2.0 mAdc, V <sub>CE</sub> = 5.0 Vdc)	h <sub>FE</sub>	200	_	450	_
Collector – Emitter Saturation Voltage ( $I_C = 10 \text{ mAdc}, I_B = 0.5 \text{ mAdc}$ )	V <sub>CE(sat)</sub>	-	_	0.25	Vdc
Base – Emitter On Voltage (I <sub>C</sub> = 2.0 mAdc, V <sub>CE</sub> = 5.0 Vdc)	V <sub>BE(on)</sub>	0.55	_	0.70	Vdc
SMALL-SIGNAL CHARACTERISTICS			•		
Output Capacitance ( $I_E = 0$ , $V_{CB} = 10$ Vdc, $f = 1.0$ MHz)	C <sub>obo</sub>	-	-	4.0	pF
Noise Figure (I <sub>C</sub> = 0.2 mAdc, V <sub>CE</sub> = 5.0 Vdc, R <sub>S</sub> = 2.0 k $\Omega$ , f = 1.0 kHz, BW = 200 Hz)	NF	-	_	10	dB

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

# **TYPICAL NOISE CHARACTERISTICS**

 $(V_{CE} = 5.0 \text{ Vdc}, T_A = 25^{\circ}C)$ 

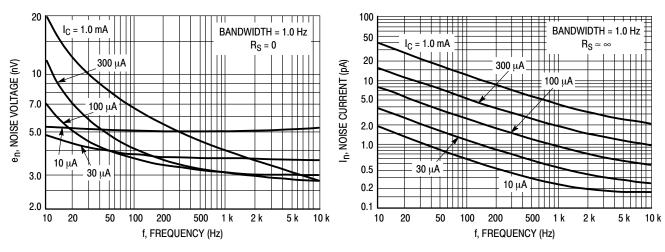


Figure 1. Noise Voltage

Figure 2. Noise Current

# **NOISE FIGURE CONTOURS**

 $(V_{CE} = 5.0 \text{ Vdc}, T_A = 25^{\circ}C)$ 

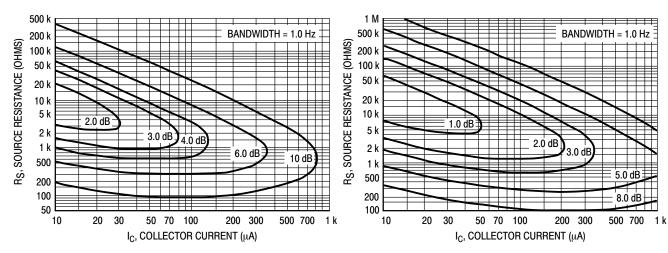


Figure 3. Narrow Band, 100 Hz

Figure 4. Narrow Band, 1.0 kHz

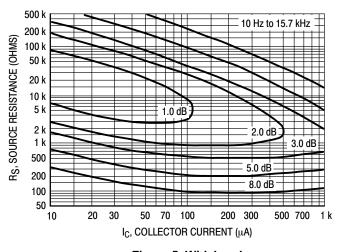


Figure 5. Wideband

Noise Figure is defined as:

$$NF = 20 \log_{10} \left( \frac{e_n^2 + 4KTR_S + I_n^2 R_S^2}{4KTR_S} \right)^{1/2}$$

 $e_n$  = Noise Voltage of the Transistor referred to the input. (Figure 3)

I = Noise Current of the Transistor referred to the input.

n (Figure 4)

K = Boltzman's Constant (1.38 x 10<sup>-23</sup> j/°K)

T = Temperature of the Source Resistance (°K)

R = Source Resistance ( $\Omega$ )

#### TYPICAL STATIC CHARACTERISTICS

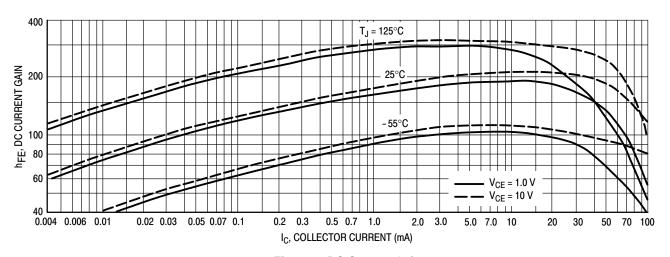


Figure 6. DC Current Gain

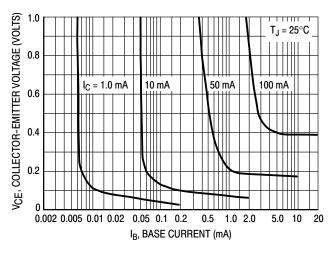
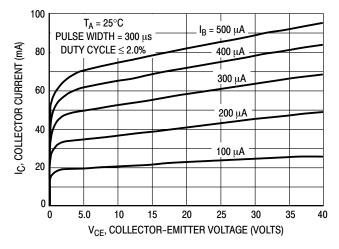


Figure 7. Collector Saturation Region



**Figure 8. Collector Characteristics** 

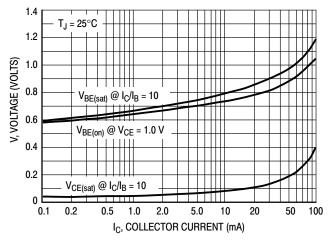
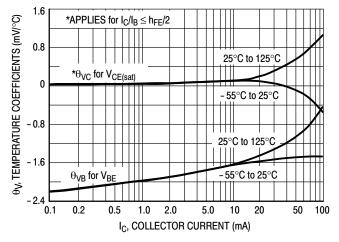


Figure 9. "On" Voltages



**Figure 10. Temperature Coefficients** 

# TYPICAL DYNAMIC CHARACTERISTICS

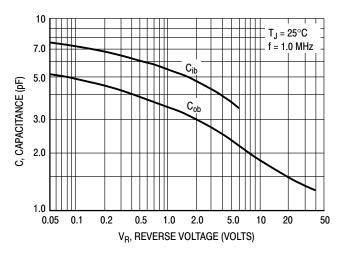
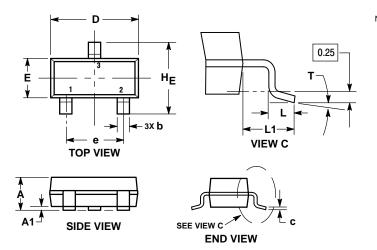


Figure 11. Capacitance

#### PACKAGE DIMENSIONS

#### SOT-23 (TO-236) CASE 318-08 **ISSUE AR**



NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. CONTROLLING DIMENSION: MILLIMETERS.
- MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH.
   MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL.

  4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH.
- PROTRUSIONS, OR GATE BURRS

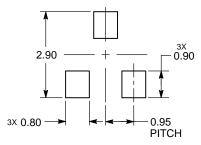
	MILLIMETERS					
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.89	1.00	1.11	0.035	0.039	0.044
A1	0.01	0.06	0.10	0.000	0.002	0.004
b	0.37	0.44	0.50	0.015	0.017	0.020
С	0.08	0.14	0.20	0.003	0.006	0.008
D	2.80	2.90	3.04	0.110	0.114	0.120
E	1.20	1.30	1.40	0.047	0.051	0.055
е	1.78	1.90	2.04	0.070	0.075	0.080
L	0.30	0.43	0.55	0.012	0.017	0.022
L1	0.35	0.54	0.69	0.014	0.021	0.027
HE	2.10	2.40	2.64	0.083	0.094	0.104
Т	0°	-	10°	0°	-	10°

STYLE 6:

PIN 1. BASE **EMITTER** 

COLLECTOR

#### **RECOMMENDED** SOLDERING FOOTPRINT\*



**DIMENSIONS: MILLIMETERS** 

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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